

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

10/539103

Appl. No. : Unassigned Confirmation No. Unassigned
Applicant : Paul Simon *et al.* JC17 Rec'd PCT/PTO 15 JUN 2005
Filed : Concurrently
TC/A.U. : Unassigned
Examiner : Unassigned

Docket No. : NL 021448 US
Customer No. : 24738

Title: Method of Producing Semiconductor Elements Using a Test
Structure

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination please amend the above-identified application as follows:

Amendments to the Specification there are no amendments in this paper.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.